



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : H04N 5/00		A1	(11) International Publication Number: WO 99/51021
			(43) International Publication Date: 7 October 1999 (07.10.99)
(21) International Application Number: PCT/IB99/00650		(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
(22) International Filing Date: 29 March 1999 (29.03.99)			
(30) Priority Data: 98400764.1 27 March 1998 (27.03.98) EP			
(71) Applicant (for all designated States except US): CANAL+ SOCIETE ANONYME [FR/FR]; 85/89, quai André Citroën, F-75711 Paris Cedex 15 (FR).			
(72) Inventors; and (75) Inventors/Applicants (for US only): MERIC, Jérôme [FR/FR]; 55, rue de Meaux, F-60300 Senlis (FR). DECLERCK, Christophe [FR/FR]; 3, rue des Ormes-Dancourt, F-28210 Senantes (FR).			
(74) Agents: COZENS, Paul, Dennis et al.; Mathys & Squire, 100 Gray's Inn Road, London WC1X 8AL (GB).		Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.	

(54) Title: MEMORY MANAGEMENT IN A RECEIVER/DECODER

(57) Abstract

A device management module (11) particularly for use in a receiver/decoder for a broadcast digital television system in which received signals are passed through a receiver to the receiver/decoder and thence to a television set. The module (11) couples incoming messages from port units (10) to application modules (12). A memory (13) coupled to the unit (11) has a buffer area (13-B) and a FIFO area (13-F), controlled by a buffer controller/handler (14-B) and a FIFO controller/handler (14-F) respectively. An incoming message can be passed into a buffer, and retrieved from the buffer by the application to which it is directed; two different operating modes are available for this. Alternatively, such a message can be passed to a FIFO; the FIFO handler acts as a low-level application which can pass the message on to a high-level application without that application having to take any action and without having to wait for the full message to be received. Messages passing through the FIFO are may be combined into an MPEG signal stream.

